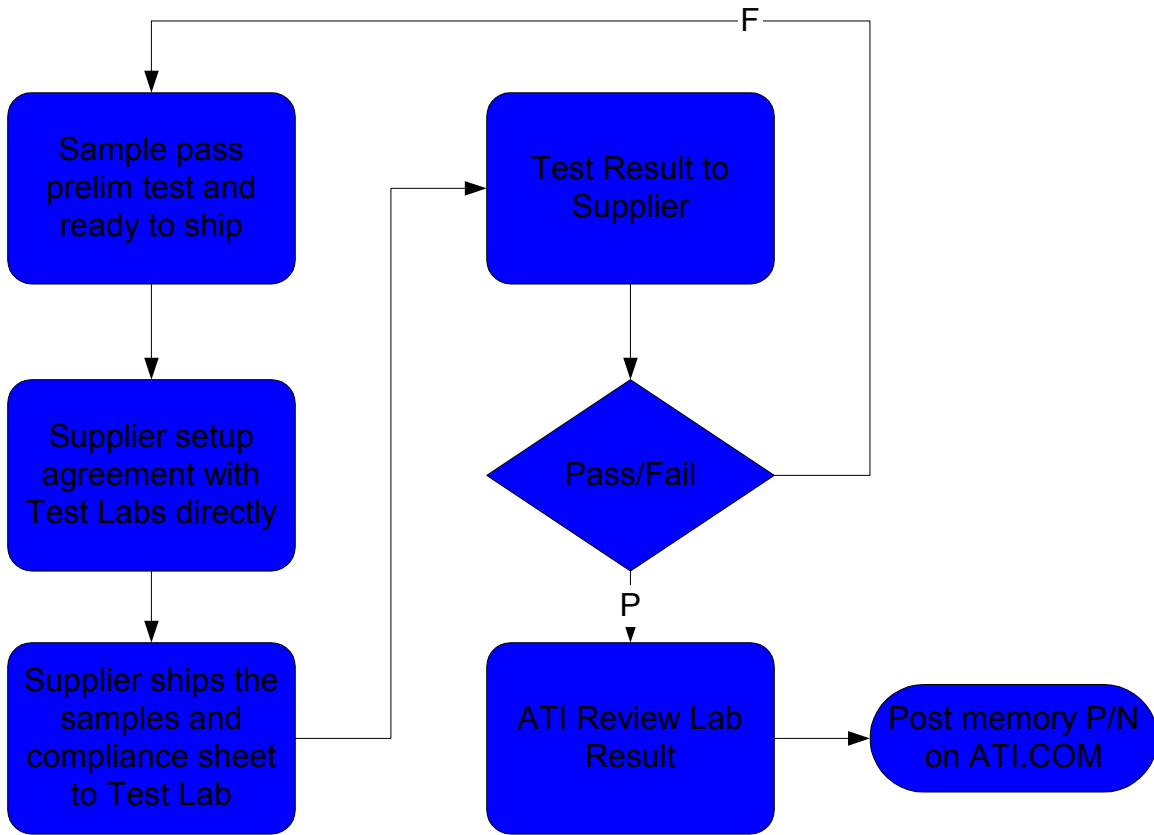


ATI RADEON™ IGP Memory Validation Process

The objective of the RADEON™ IGP memory validation process is to verify a DDR module's compliance and performance on RADEON™ IGP reference systems. Modules of different suppliers, speeds, and densities are tested at various temperature and voltage corners according to this validation process.



Details of ATI Radeon IGP Validation Process

1. DDR modules must pass the supplier's internal testing before submitting to test lab for validation.
2. An agreement with the approved test lab is ratified under the terms and conditions of the supplier.

Approved Lab

Advanced Validation Labs
17800 Newhope Street Ste. B
Fountain Valley, CA 92708
Phone: 714-435-2630
services@validationlabs.com
<http://www.validationlabs.com/>



3. Test samples along with a compliance sheet need to be submitted directly to the test lab under the approved agreement.
4. Validation results after completion from the test lab are reported directly to supplier.
5. ATI will review the validation data and the following action will be defined:

If the part passes the validation tests, ATI will post the validated part onto the web page.

If the part fails the validation tests, the supplier will correct the problem and resubmit samples to the test lab for re-validation.